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10/626,965

07/25/2003

Rudolf J. Hofmeister

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3965

7590

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EXAMINER

PHAN, HANH

ART UNIT

PAPER NUMBER

2613

MAIL DATE

DELIVERY MODE

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PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/626,965

Applicant(s)

HOFMEISTER ET AL.

Examiner

Hanh Phan

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 25 May 2007.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-25, 27-32 and 37-42 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-7, 11-19, 23, 24, 29, 31 and 37-42 is/are rejected.
- 7) ☒ Claim(s) 8-10, 20-22, 27, 28, 30 and 32 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- ☒ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☐ Information Disclosure Statement(s) (PTO/SB/08).
Paper No(s)/Mail Date _____.
- ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- ☐ Notice of Informal Patent Application
- ☐ Other: _____.

DETAILED ACTION

1. This Office Action is responsive to the Amendment filed on 05/25/2007.

Claim Objections

2. Claim 32 is objected to because of the following informalities:

In claim 32, line 1, the phrase "The transceiver of claim 32" should be changed to --The transceiver of claim 31--. Appropriate correction is required.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 1, 11, 23, 24, 29, 37, 38, 41 and 42 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sakai et al (US Patent No. 5,557,437) in view of Miyachi et al (US Patent No. 5,920,414).

Regarding claims 1, 23, 24 and 29, referring to Figure 1, Sakai et al teaches a transceiver for use in connecting a host with an optical network, the transceiver comprising:

an input port (i.e., input port receiving an electrical signal, Fig. 1) that receives electrical signals from a host, the input port being in communication with

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an optical transmitter (i.e., Electrical/optical converter 16, Fig. 1) for generating an optical signal for transmission to the optical network (i.e., col. 4, lines 49-67, col. 5, lines 1-67 and col. 6, lines 1-26);

an output port (i.e., output port for outputting an electrical signal, Fig. 1) that sends electrical signals to the host, the output port being in communication with an optical receiver (i.e., Optical/electrical converter 1, Fig. 1) that receives optical signals from the optical network and converts the optical signals into electrical signals (i.e., col. 4, lines 49-67, col. 5, lines 1-67 and col. 6, lines 1-26);

a loopback path (i.e., a loopback 24, Fig. 1) for selectively coupling an incoming electrical signal from the input port to the output port (i.e., col. 4, lines 49-67, col. 5, lines 1-67 and col. 6, lines 1-26).

Sakai differs from claims 1, 23, 24 and 29 in that he fails to teach a pass-through port and a pass-through path configured to selectively couple an incoming electrical signal from the input port to the pass-through port. However, Miyachi et al teaches a pass-through port and a pass-through path configured to selectively couple an incoming electrical signal from the input port to the pass-through port (i.e., Fig. 13, col. 16, lines 63-67 and col. 17, lines 1-35). Based on this teaching, it would have been obvious to one having skill in the art at the time the invention was made to incorporate the pass-through port and a pass-through path configured to selectively couple an incoming electrical signal from the input port to the pass-through port as taught by Miyachi et al in the system of Sakai et al. One of ordinary skill in the art would have

been motivated to do this since allowing providing an improved optical line terminal for a network wherein the optical line terminal protects against network component failures.

Regarding claim 11, Sakai et al further teaches the loopback path is configured for diagnosing the operation of a host, the transceiver, and optical devices interconnecting the host and the transceiver (i.e., Fig. 1).

Regarding claims 37, 38, 41 and 42, the combination of Sakai et al and Miyachi et al teaches the transceiver is configured to be daisy-chained with another transceiver by way of the pass-through port (i.e., Fig. 13 of Miyachi et al).

5. Claims 2-7 and 25 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sakai et al (US Patent No. 5,557,437) in view of Miyachi et al (US Patent No. 5,920,414) and further in view of Kato et al (Pub. No.: US 2002/0021468).

Regarding claims 2-7 and 25, the combination of Sakai et al and Miyachi et al differs from claims 2-7 and 25 in that it fails to teach an integrated chip with a post amplifier and laser driver. Kato teaches an integrated chip with a post amplifier and laser driver (i.e., Figs. 2 and 3, pages 3 and 4, paragraphs [0038]-[0048]). Based on this teaching, it would have been obvious to one having skill in the art at the time the invention was made to incorporate the integrated chip with a post amplifier and laser driver as taught by Kato in the system of the combination of Sakai et al and Miyachi et al. One of ordinary skill in the art would have been motivated to do this since allowing providing increasing the power level of signal to a desired level and reducing size, weight and cost of the system.

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6. Claims 12-14 and 31 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sakai et al (US Patent No. 5,557,437) in view of Platt (US Patent No. 5,802,073).

Regarding claims 12 and 31, referring to Figure 1, Sakai et al teaches a transceiver for use in connecting an optical network to a host, the transceiver comprising:

an optical transmitter (i.e., Electrical /optical converter 16, Fig. 1) generating an optical output in response to an electrical input signal applied to an input of the optical transmitter;

an optical receiver (i.e., Optical /electrical converter 1, Fig. 1) generating an electrical output signal at a receiver output in response to an optical input; and

a loopback path (i.e., a loopback 20, Fig. 1) for selectively coupling the electrical output signal from the optical receiver (i.e., O/E converter 1, Fig. 1) to the input of the optical transmitter (i.e., (E/O converter 16, Fig. 1, col. 4, lines 49-67, col. 5, lines 1-67 and col. 6, lines 1-26).

Sakai et al differs from claims 12 and 31 in that he fails to teach a pass-through port and a pass-through path configured to selectively couple the electrical output signal from the optical receiver to the pass-through port. However, Platt teaches a transceiver comprises a transmitter 106b and a receiver 106a (Fig. 1B). Platt further teaches a pass-through port (i.e., input terminals of the BIST, Fig. 1B) and a pass-through path (i.e. a bus 140, Fig. 1B) configured to selectively couple the electrical output signal from the receiver 106a to the pass-through port (i.e., col. 3, lines 35-67, col. 4, lines 1-67 and col. 5, lines 1-10). Based on this teaching, it would have been

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obvious to one having skill in the art at the time the invention was made to incorporate the pass-through port and the pass-through path configured to selectively couple the electrical output signal from the receiver to the pass-through port as taught by Platt in the system of Sakai et al. One of ordinary skill in the art would have been motivated to do this since allowing providing an in-system diagnostic test for an integrated circuit.

Regarding claim 13, Sakai et al further teaches the loopback path is configured for bypassing a transceiver output port and coupling the electrical output signal to the input of the optical transmitter (i.e., Fig. 1).

Regarding claim 14, Sakai et al further teaches the loopback path is configured for diagnosing the operation of a remote host, the transceiver, and optical devices interconnecting the remote host and the transceiver (i.e., Fig. 1).

7. Claims 15-19, 39 and 40 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sakai et al (US Patent No. 5,557,437) in view of Platt (US Patent No. 5,802,073) and further in view of Kato et al (Pub. No.: US 2002/0021468).

Regarding claim 15, the combination of Sakai et al and Platt differs from claim 15 in that it fails to teach an integrated chip with a post amplifier and laser driver. Kato teaches an integrated chip with a post amplifier and laser driver (i.e., Figs. 2 and 3, pages 3 and 4, paragraphs [0038]-[0048]). Based on this teaching, it would have been obvious to one having skill in the art at the time the invention was made to incorporate the integrated chip with a post amplifier and laser driver as taught by Kato in the system of the combination of Sakai et al and Platt. One of ordinary skill in the art would have

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been motivated to do this since allowing providing increasing the power level of signal to a desired level and reducing size, weight and cost of the system.

Regarding claim 16, the combination of Sakai et al, Platt and Kato teaches the loopback path does not pass through the post amplifier or the laser driver (i.e., Fig. 1 of Sakai et al and Figs. 2 and 3 of Kato).

Regarding claims 17-19, the combination of Sakai et al, Platt and Kato teaches the loopback path passes through at least one of the receiver eye opener and the transmitter eye opener (i.e., Fig. 1 of Sakai et al and Figs. 2 and 3 of Kato).

Regarding claims 39 and 40, the combination of Sakai et al, Platt and Kato teaches the transceiver is configured to be daisy-chained with another transceiver by way of the pass-through port (i.e., Fig. 1B of Platt).

Allowable Subject Matter

8. Claims 8-10, 20-22, 27, 28, 30 and 32 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Response to Arguments

9. Applicant's arguments with respect to claims 1-25, 27-32 and 37-42 have been considered but are moot in view of the new ground(s) of rejection.

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Conclusion

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hanh Phan whose telephone number is (571)272-3035.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jason Chan, can be reached on (571)272-3022. The fax phone number for the organization where this application or proceeding is assigned is (571)273-8300.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703)305-4700.


HANH PHAN
PRIMARY EXAMINER